

Parallel Programming Principle and Practice

Lecture 7 —

Threads programming with TBB



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Outline

- □ Intel Threading Building Blocks
- Task-based programming
- Task Scheduler
- Scalable Memory Allocators
- Concurrent Containers
- Synchronization Primitives





Ways to Improve Naïve Implementation

Programming with OS Threads can get complicated and error-prone, even for the pattern as simple as for-loop

Problems with Naïve Implementation	What You Could Do to Improve It
Works with <i>fixed number of threads</i>	Implement a function which determines the ideal number of worker threads
The implementation is <i>not</i> portable	Implement wrapper functions with code specific to each supported OS
The solution is <i>not re-</i> <i>usable</i>	Abstract the iteration space and re-write all the loops to comply with it
Potentially <i>poor</i> <i>performance</i> due to work- load imbalance	Implement thread-pool and use heuristics to balance the work-load between worker threads
The solution is <i>not</i> composable	Wellcontinue adding more codedoing testingand tuning



Task-basked Programming Sector A Better Approach to Parallelism

- Portable task-based technologies
 - Intel® Threading Building Blocks (Intel® TBB)
 - lets you easily write parallel C++ programs that take full advantage of multicore performance, that are portable and composable, and that have future-proof scalability.
 - C++ template library: general task-based programming, concurrent data containers, and more ...





Key Feature

- It is a template library intended to ease parallel programming for C++ developers
 - Relies on generic programming to deliver high performance parallel algorithms with broad applicability
- □ It provides a *high-level abstraction* for parallelism
- □ It facilitates scalable performance
 - Strives for efficient use of cache, and balances load
 - Portable across Linux*, Mac OS*, Windows*, and Solaris*
- Can be used in concert with other packages such as native threads and OpenMP (fighting for thread, tbb, openmp)
- Open source and licensed versions available



Typical Serial Program	Ideal Parallel Program	Issues
Algorithms	Parallel Algorithms	Require many code changes when developed from scratch: often it takes a threading expert to get it right
Data Structures	Thread-safe and scalable Data Structures	Serial data structures usually require global locks to make operations thread-safe
Dependencies	 Minimum of dependencies Efficient use of synchronization primitives or thread local storage 	Too many dependencies → expensive synchronization → poor parallel performance
Memory Management	Scalable Memory Manager	Standard memory allocator is often inefficient in multi-threaded app



Task-based Programming Advantages



	OS Threads	Intel® Cilk™ Plus Intel® Threading Building Blocks
Forward-scaling	Takes a threading expert to implement a scalable solution	Allow thinking at higher level and produce implementations independent of number of CPUs
Portability	Non-portable, requires extra coding, maintenance, and testing	Portable across many platforms
Flexibility	Requires <i>extra effort</i> to implement reusable solution	Broadly applicable by design
Performance	Requires a threading expert and special knowledge to get it right	Designed for high performance
Composability	Cross-component coordination is required (added coding, testing, and tuning)	Support <i>nested parallelism</i> and can be used together
Conclusion:	An efficient solution using OS threads requires expertise and leads to a significant re-design	Task-based solution often can speed up your app with a minimal code changes



Implementing Common Paralles

Parallel Program Components	Intel [®] Parallel Building Blocks
Parallel Algorithms	Intel [®] Cilk [™] Plus and Intel [®] Threading Bulling Blocks (Intel [®] TBB) parallel loops , parallel functions , parallel recursion , parallel pipeline
Thread-safe and Scalable Data Structures	Intel TBB concurrent containers
Dependencies	Intel TBB flow graph
Thread-Local Storage	Intel Cilk Plus reducers Intel TBB thread-local storage
Synchronization Primitives	Intel TBB exception-safe locks, condition variables, and atomics
Scalable Memory Manager	Intel TBB scalable memory allocator and false- sharing free allocator





Intel® TBB online







limitation

- □ TBB is not intended for
 - I/O bound processing
 - Real-time processing
- General limitations
 - Direct use only from C++
 - Distributed memory not supported (target is desktop)
 - Requires more work than sprinkling in pragmas, for example OpenMP



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Task-based programming

- Tasks are light-weight entities at user-level
 - TBB parallel algorithms map tasks onto threads automatically
 - Task scheduler manages the thread pool
 - Scheduler is *unfair* to favor tasks that have been most recent in the cache
- Oversubscription and undersubscription of core resources is prevented by task-stealing technique of TBB scheduler





Generic Parallel Algorithms

Loop parallelization

- parallel_for and parallel_reduce Load balanced parallel execution of fixed number of independent loop iterations
- parallel_scan Template function that computes parallel prefix (y[i] = y[i-1] op x[i])

Parallel Algorithms for Streams

- parallel_do Use for unstructured stream or pile of work; Can add additional work to pile while running
- parallel_for_each parallel_do without an additional work feeder





Generic Parallel Algorithms

Parallel Algorithms for Streams

pipeline / parallel_pipeline

- Linear pipeline of stages you specify maximum number of items that can be in flight
- Each stage can be parallel or serial in-order or serial out-of-order. Stage (filter) can also be thread-bound
- Uses cache efficiently: Each worker thread flies an item through as many stages as possible; Biases towards finishing old items before tackling new ones

Others

- parallel_invoke Parallel execution of a number of user-specified functions
- parallel_sort Comparison sort with an average time complexity O(N Log(N)); When worker threads are available parallel_sort creates subtasks that may be executed concurrently





The parallel_for Template

template <typename Range, typename Body> void parallel_for(const Range& range, const Body &body);

Requires definition of

- A range type to iterate over
 - Must define a copy constructor and a destructor
 - Defines is_empty ()
 - Defines is_divisible ()
 - Defines a splitting constructor, R(R &r, split)
- A body type that operates on the range (or a subrange)
 - Must define a copy constructor and a destructor
 - Defines operator()





Body is Generic

Requirements for parallel_for Body

Body::Body(const Body&)	Copy constructor
Body::~Body()	Destructor
<pre>void Body::operator() (Range& subrange) const</pre>	Apply the body to subrange.

parallel_for partitions original range into subranges, and deals out subranges to worker threads in a way that

- Balances load
- Uses cache efficiently
- Scales





Range is Generic

Requirements for parallel_for Range

R::R (const R&)	Copy constructor
R::~R()	Destructor
<pre>bool R::is_empty() const</pre>	True if range is empty
<pre>bool R::is_divisible() const</pre>	True if range can be partitioned
R::R (R& r, split)	Splitting constructor; splits r into two subranges

- □ Library provides predefined ranges
 - blocked_range and blocked_range2d
- You can define your own ranges



An Example using parallel_for (1 of 4)

Independent iterations and fixed/known bounds

Sequential code starting point

```
const int N = 100000:
void change_array(float array, int M) {
 for (int i=0; i<M; i++) {
  array[i] *= 2;
int main() {
 float A[N];
 initialize_array(A);
 change_array(A,N);
 return 0;
```



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An Example using parallel_for (2 of 4)

Include and initialize the library

int main() {
 float A[N];
 initialize_array(A);
 change_array(A,N);
 return 0;

blue = original code green = provided by TBB red = boilerplate for library #include "tbb/task_scheduler_init.h"
#include "tbb/blocked_range.h"
#include "tbb/parallel_for.h"
using namespace tbb;

int main() {
 task_scheduler_init init;
 float A[N];
 initialize_array(A);
 parallel_change_array(A,N);
 return 0;
}



An Example using parallel_for (3 of 4)

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```
blue = original code
    Use the parallel_for algorithm
                                                       green = provided by TBB
                                                       red = boilerplate for library
void change_array(float array, int M) {
 for (int i=0; i<M; i++) {
  array[i] *= 2;
                     class ChangeArrayBody {
                       float *array;
                      public:
                       ChangeArrayBody (float *a): array(a) {}
                       void operator() ( const blocked_range <int>& r ) const {
                             for (int i=r.begin(); i != r.end(); i++) {
                                 array[i] *= 2;
                     };
                     void parallel_change_array(float *array, int M) {
                       parallel_for (blocked_range <int>(0,M),
                                 ChangeArrayBody(array), auto_partitioner());
```

An Example using parallel_for (4 of 4)

□ Use the parallel_for algorithm

blue = original code green = provided by TBB red = boilerplate for library

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```
class ChangeArrayBody {
    float *array;
public:
    ChangeArrayBody (float *a): array(a) {}
    void operator() ( const blocked range <int>& r ) const{
        for (int i = r.begin(); i != r.end(); i++ ) {
            array[i] *= 2;
};
void parallel change array(float *array, int M) {
 parallel for (blocked range <int>(0, M),
               ChangeArrayBody (array) ,
               auto partitioner());
```



Parallel algorithm usage example



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Task Scheduler

- Task scheduler is the engine driving Intel® Threading Building Blocks
 - Manages thread pool, hiding complexity of native thread management
 - Maps logical tasks to threads
- Parallel algorithms are based on task scheduler interface
- Task scheduler is designed to address common performance issues of parallel programming with native threads

Problem	Intel [®] TBB Approach
Oversubscription	One scheduler thread per hardware thread
Fair scheduling	Non-preemptive unfair scheduling
High overhead	Programmer specifies tasks, not threads.
Load imbalance	Work-stealing balances load

Two Execution Orders

Depth First (stack)



Small space Excellent cache locality No parallelism



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Large space Poor cache locality Maximum parallelism



SCIS Work Depth First; Steal Breadth First



victim thread



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THREAD 1

32 44 9 26 31 57 3 19 55 29 27 1 20 5 42 62 25 51 49 15 54 6 18 48 10 2 60 41 14 47 24 36 37 52 22 34 35 11 28 8 13 43 53 23 61 38 56 16 59 17 50 7 21 45 4 39 33 40 58 12 30 0 46 63

Thread 1 starts with the initial data





























SCIS

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SCIS

CGCL



The parallel_reduce Template

template <typename Range, typename Body> void parallel_reduce (const Range& range, Body &body);

Requirements for parallel_reduce Body

Body::Body(const Body&, split)

Body::~Body()

void Body::operator() (Range& subrange) const

```
void Body::join( Body& rhs );
```

Splitting constructor

Destructor

Accumulate results from subrange

Merge result of *rhs* into the result of this.



Numerical Integration Example (1 of 3)



```
static long num_steps=100000;
double step, pi;
```

```
void main(int argc, char*
argv[])
{ int i;
   double x, sum = 0.0;
```

```
step = 1.0/(double) num_steps;
for (i=0; i< num_steps; i++) {
    x = (i+0.5)*step;
    sum += 4.0/(1.0 + x*x);
}
pi = step * sum;
printf("Pi = %f\n",pi);
```



parallel_reduce Example (2 of 3)

```
green = provided by TBB
#include "tbb/parallel reduce.h"
                                                 red = boilerplate for library
#include "tbb/task scheduler init.h"
#include "tbb/blocked range.h"
using namespace tbb;
int main(int argc, char* argv[])
{
  double pi;
  double width = 1./(double)num steps;
  MyPi step((double *const)&width);
  task scheduler init init;
  parallel reduce (blocked range<size t>(0,num steps), step,
                                                      auto partitioner() );
  pi = step.sum*width;
  printf("The value of PI is %15.12f\n",pi);
  return 0;
ł
```



SCLS

blue = original code

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parallel_reduce Example (3 of 3)

```
blue = original code
                                                  green = provided by TBB
class MyPi {
                                                  red = boilerplate for library
  double *const my step;
public:
  double sum;
  void operator() ( const blocked range<size t>& r ) {
    double step = *my step;
    double x:
    for (size t i=r.begin(); i!=r.end(); ++i)
    Ł
       x = (i + .5) * step;
                                                       accumulate results
       sum += 4.0/(1.+ x*x);
    }
  }
  MyPi( MyPi& x, split ) : my step(x.my step), sum(0) {}
                                                                    join
  void join( const MyPi& y ) {sum += y.sum;}
  MyPi(double *const step) : my step(step), sum(0) {}
```



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Task cancellation avoids unneeded work



There is a whole class of application that can benefit from ability to cancel work early







Task cancellation example





Uncaught exceptions cancel task execution





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Scalable Memory Allocators

- Serial memory allocation can easily become a bottleneck in multithreaded applications
 - Threads require mutual exclusion into shared heap
- **False sharing threads accessing the same cache line**
 - Even accessing distinct locations, cache line can ping-pong
- Intel® Threading Building Blocks offers two choices for scalable memory allocation
 - Similar to the STL template class std::allocator
 - scalable_allocator
 - Offers scalability, but not protection from false sharing
 - Memory is returned to each thread from a separate pool
 - cache_aligned_allocator
 - Offers both scalability and false sharing protection



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Concurrent Containers

- TBB Library provides highly concurrent containers
 - STL containers are not concurrency-friendly: attempt to modify them concurrently can corrupt container
 - Standard practice is to wrap a lock around STL containers
 - Turns container into serial bottleneck
- Library provides fine-grained locking or lockless implementations
 - Worse single-thread performance, but better scalability
 - Can be used with the library, OpenMP, or native threads



Concurrent Containers Key Features

concurrent_hash_map <Key,T,Hasher,Allocator>

- Models hash table of std::pair <const Key, T> elements
- Maps Key to element of type T
- · User defines Hasher to specify how keys are hashed and compared
- Defaults: Allocator=tbb::tbb_allocator

concurrent_unordered_map<Key,T,Hasher,Equality,Allocator>

- Permits concurrent traversal and insertion (no concurrent erasure)
- Requires no visible locking, looks similar to STL interfaces
- Defaults: Hasher=tbb::tbb_hash, Equality=std::equal_to, Allocator=tbb::tbb_allocator

concurrent_vector <T, Allocator>

- Dynamically growable array of T: grow_by and grow_to_atleast
- cache_aligned_allocator is a default allocator

concurrent_queue <T, Allocator>

- · For single threaded run concurrent_queue supports regular "first-in-first-out" ordering
- If one thread pushes two values and the other thread pops those two values they will come out in the order as they were pushed
- cache_aligned_allocator is a default allocator

concurrent_bounded_queue <T, Allocator>

 Similar to concurrent queue with a difference that it allows specifying capacity. Once the capacity is reached 'push' will wait until other elements will be popped before it can continue.

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Synchronization Primitives

- Parallel tasks must sometimes touch shared data
 - When data updates might overlap, use mutual exclusion to avoid race
- High-level generic abstraction for HW atomic operations
 - Atomically protect update of single variable
- Critical regions of code are protected by scoped locks
 - The range of the lock is determined by its lifetime (scope)
 - Leaving lock scope calls the destructor, making it exception safe
 - Minimizing lock lifetime avoids possible contention
 - Several mutex behaviors are available





Atomic Execution

- atomic <T>
 - T should be integral type or pointer type
 - Full type-safe support for 8, 16, 32, and 64-bit integers
 Operations

`= x' and `x = '	read/write value of x
x.fetch_and_store (y)	z = x, x = y, return z
x.fetch_and_add (y)	z = x, x += y, return z
x.compare_and_swap (y,p)	z = x, if (x==p) x=y; return z

```
atomic <int> i;
. . .
int z = i.fetch_and_add(2);
```





Mutex Flavors

- spin_mutex
 - Non-reentrant, unfair, spins in the user space
 - VERY FAST in lightly contended situations; use if you need to protect very few instructions
- queuing_mutex
 - Non-reentrant, fair, spins in the user space
 - Use Queuing_Mutex when scalability and fairness are important
- queuing_rw_mutex
 - Non-reentrant, fair, spins in the user space
- spin_rw_mutex
 - Non-reentrant, fair, spins in the user space
 - Use ReaderWriterMutex to allow non-blocking read for multiple threads





One last question...

How do I know how many threads are available?

- Do not ask!
 - Not even the scheduler knows how many threads really are available
 - There may be other processes running on the machine
 - Routine may be nested inside other parallel routines
- **Focus on dividing your program into tasks of sufficient size**
 - Task should be big enough to amortize scheduler overhead
 - Choose decompositions with good depth-first cache locality and potential breadth-first parallelism
- Let the scheduler do the mapping





References

- ☐ The content expressed in this chapter is come from
 - berkeley university open course (http://parlab.eecs.berkeley.edu/2010bootcampagenda, Shared Memory Programming with TBB, Michael Wrinn)
 - http://software.intel.com/en-us/courseware
 - IDF2012: Task Parallel Evolution and Revolution Intel Cilk Plus and Intel Threading Building Blocks

